09/914505

| Subclass | ISSUE CLASSIFICATION |
|---|---|
| Class | |

FILED UNDER 35 U.S.C. 371

PATENT NUMBER

# U.S. UTILITY Patent Application

| O.I.P.E. | PATENT DATE |
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| SCANNED Hkml Q.A. lc | |

| APPLICATION NO. | CONT/PRIOR | CLASS | SUBCLASS | ART UNIT | EXAMINER |
|---|---|---|---|---|---|
| 09/914505 | D F | 341 ~~M~~ 369 | ~~[illegible]~~ 59.22 | ~~2819~~ 2653 | Chin - Vuong |

APPLICANTS: Yukio Koyanagi

TITLE: Interpolation circuit

PTO-2040 12/99

## ISSUING CLASSIFICATION

| ORIGINAL | | CROSS REFERENCE(S) | |
|---|---|---|---|
| CLASS | SUBCLASS | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) |
| | | | |
| INTERNATIONAL CLASSIFICATION | | | |
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☐ Continued on Issue Slip Inside File Jacket

| ☐ TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
|---|---|---|---|---|---|
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| ☐ The term of this patent subsequent to _____ (date) has been disclaimed. | | | | NOTICE OF ALLOWANCE MAILED | |
| ☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ | | | | ISSUE FEE | |
| | | | | Amount Due | Date Paid |
| ☐ The terminal ____months of this patent have been disclaimed. | | | | ISSUE BATCH NUMBER | |

(Assistant Examiner) (Date)

(Primary Examiner) (Date)

(Legal Instruments Examiner) (Date)

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